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11-25-02

Docket No.: GR 00 P 1281

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By: Markus NOLFF Date: TECHNOLOGY CENTER 2800
November 14, 2002

Inventor : Harald Bachhofer et al.
Applic. No. : 09/783,187
Filed : February 14, 2001
Title : Memory Configuration and Method for Reading a State from and
Storing a State in a Ferroelectric Transistor
Examiner : Roy Karl Potter - Art Unit: 2822

RESPONSE

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

Sir:

Responsive to the Office Action dated October 3, 2002, the following remarks are made:

In deference to the restriction requirement on pages 2 and 3 of the above-identified Office Action, applicants elect **group II, claims 8-10** for prosecution at this time.

Applicants request a rejoinder under MPEP §821.04.

In view of the foregoing, the early issuance of an Action on the merits of claims is solicited.

Petition for extension is herewith made. The extension fee for response within a period of one month pursuant to Section 1.136(a) in the amount of \$110.00 in accordance with Section 1.17 is enclosed herewith.

Respectfully submitted,

Markus NOLFF
For Applicants

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REG. NO. 37,006

Date: November 14, 2002

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